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## **IP SERVICES**



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## (WO/2002/075650) HARDWARE DESIGN USING EVOLUTION ALGORITHMS

Biblio. Data

Description

Claims

National Phase

**Notices** 

**Documents** 

## Latest bibliographic data on file with the International Bureau

Publication Number: WO/2002/075650

International Application No.: PCT/GB2002/001157

**Publication Date:** 

26.09.2002

International Filing Date:

13.03.2002

Chapter 2 Demand Filed: 10.10.2002

G06F 17/50 (2006.01), G06N 3/12 (2006.01)

Int. Class.: Applicants:

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CM, CN, CO, CR, CU, CY, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, FR, GA, GB, GD, GE, GH, GM, GN, GQ, GR, GW, HR, HU, ID, IE, IL, IN, IS, IT, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS. LT, LU, LV, MA, MC, MD, MG, MK, ML, MN, MR, MW, MX, MZ, NE, NL, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, SN, SZ, TD, TG, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZM, ZW only).

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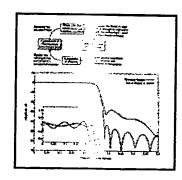
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Priority Data: 0106459.1 15.03.2001 GB

Title: Abstract: HARDWARE DESIGN USING EVOLUTION ALGORITHMS The design of a hardware component such as a digital filter can be

optimised by taking an initial population of filter designs and encoding them as chromosomes. The fitness of each chromosome is then evaluated and parent chromosomes are then selected based on the fitness criteria. Offspring chromosomes are then generated ussing genetic operations such as mutation and crossover from the pool of offspring, and optionally parents, individuals are selected to survive using a combination of Pareto

fronts based on non-dominated individuals and clustering. The process is repeated or until a termination criteria is satisfied.



Designated States:

AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS,

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SE, TR)

African Intellectual Property Organization (OAPI) (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR,

NE, SN, TD, TG).

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**Publication Language:** 

English (EN)